Faster Clustering Coefficient Using Vertex Covers

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Abstract—Clustering coefficients, also called triangle counting, is a widely-used graph analytic for measuring the closeness in which vertices cluster together. Intuitively, clustering coefficients can be thought of as the ratio of common friends versus all possible connections a person might have in a social network. The best known time complexity for computing clustering coefficients uses adjacency list intersection and is \(O(V \cdot d_{\text{max}}^2)\), where \(d_{\text{max}}\) is the size of the largest adjacency list of all the vertices in the graph. In this work, we show a novel approach for computing the clustering coefficients in an undirected and unweighted graphs by exploiting the use of a vertex cover, \(V' \subseteq V\). This new approach reduces the number of times that a triangle is counted by as many as 3 times per triangle. The complexity of the new algorithm is \(O(V \cdot d_{\text{max}}^2 + t_{\text{VC}})\), where \(d_{\text{max}}\) is the size of the largest adjacency list in the vertex cover and \(t_{\text{VC}}\) is the time needed for finding the vertex cover. Even for a simple vertex cover algorithm this can reduce the execution time 10%–30% while counting the exact number of triangles (3-circuits). We extend the use of the vertex cover to support counting squares (4-circuits) and clustering coefficients for dynamic graphs.

Keywords—Graph Algorithms; Social Network Analysis; Network Science;

I. INTRODUCTION

Clustering coefficients is a graph analytic that states how tightly bound vertices are in a graph [29]. The tightness is measured by computing the number of closed triangles in the graph, which can then imply the small-world property. Computing the clustering coefficients has been applied to many types of networks: communication [26], collaboration [27], social [27], citation [23], and biological [7]. In social networks, one can think of the local clustering coefficients as the ratio of actual mutual acquaintances versus all possible mutual acquaintances. Applications using clustering coefficients include DIMES [26], which is a distributed platform used for providing an accurate, and comprehensive map of the Internet and automatic Web-Spam detection [8].

There are two types of clustering coefficients: global and local. The global clustering coefficient is a single value computed for the entire graph, whereas the local clustering coefficient is computed per vertex. Both are computed in a similar fashion. Without the loss of generality, we consider the global clustering coefficient in this work; nonetheless our approach is applicable for computing local clustering coefficients. Table I presents the notations used in this paper.

Definition 1: Formally, the global clustering coefficient is:

\[ CC_{\text{global}} = \sum_{v \in V} CC_v = \sum_{v \in V} \frac{\text{tri}(v)}{\deg(v) \cdot (\deg(v) - 1)} \]

There are several approaches for computing clustering coefficients:

1) Enumerating over all node-triples. This has an \(O(V^3)\) upper bound complexity.
2) Using matrix multiplication. This has an \(O(V^w)\) complexity where \(w \leq 2.376\) [11].
3) Intersecting adjacency lists. This has an \(O(V \cdot d_{\text{max}}^2)\) upper bound [25] where \(d_{\text{max}}\) is the vertex with largest adjacency.

In this paper we show a novel and intuitive way to improve the adjacency list intersection approach that removes redundant list intersections, therefore reducing the run-time of the algorithm. We accomplish this by only intersecting the adjacency lists of vertices that are marked in an arbitrary vertex cover.

We then extend our method for computing a circuit of any length and illustrate this with circuits of length 4. We show that our new approach can also be applied to dynamic graphs.

The remainder of the paper will be structured as follows. Section II discusses the related work and discusses real-world graph properties and introduces vertex covers. In Section III, we present our new algorithm for counting 3-circuits (clustering coefficients), 4-circuits, and extend the vertex cover approach for computing clustering coefficients for dynamic graphs. In Section IV, we discuss our experimental methodology and present quantitative results. Finally, in Section V, we present our conclusions and discuss future work.

II. RELATED WORK

In this section we review the literature that addresses the challenge of computing clustering coefficients for large dynamic graphs. These approaches take into account optimizations such as parallelization, approximation, and dynamic algorithms that make only local modifications to the graphs. These optimizations are crucial in order to analyze social networks such as Facebook [1] and Twitter [2] which can potentially have million of members and thousands of
Further research on graph properties advanced when it was shown in [6], [9], [16] that the world wide web follows a power law distribution on the number of adjacent edges a vertex has. These graphs are considerably sparse with an average degree that is constant. For example, the average degree for Facebook [1] is 189 [28], which is very small compared to the number of vertices in the graph. This power law distribution is important for an analytic such as clustering coefficients where the time complexity is based on the vertex with the highest degree.

B. Vertex Covers

A Vertex Cover is a set \( \hat{V} \subseteq V \) such that for all \((a, b) \in E\), either \(a \in \hat{V}\) or \(b \in \hat{V}\) (or possibly both). A minimal vertex cover is the smallest \( \hat{V} \subseteq V \) meeting the requirement above. Finding a vertex cover is a well studied problem [12], [19], [21], [5], [18]. Finding the minimal vertex cover is known to be NP-Complete (NPC) [17], [20]. We do not extend the discussion on this as it not relevant for this paper.

While finding the minimal vertex cover is intractable, there are numerous algorithms that can find some vertex cover in polynomial time, including linear time [12], [19], [21], [5]. In [21] a parallel algorithm for computing the vertex cover based on the A* formulation is given.

A theoretic PRAM algorithm is given in [19] where the vertex cover is found in iterative fashion using an Euler circuit. A total of \( O(V + E) \) processors are required and the time complexity is \( O(\log^3(E)) \).

III. VERTEX COVER CLUSTERING COEFFICIENTS

In this section we show a new algorithm for finding the clustering coefficients using vertex covers that avoid wasteful neighbor intersections.

A. CLUSTERING COEFFICIENTS FOR STATIC GRAPHS

In the following subsections we discuss both theoretical and practical implications of the the vertex cover. For simplicity, assume that a vertex cover of \( \hat{V} \) of \( G = (V, E) \) is given.

Consider a triangle made up of three vertices \( u, v, w \). These vertices may potentially have additional adjacencies; for our discussion these edges are not relevant. Now consider a vertex cover over this triangle. Fig. 1 depicts all legal vertex covers of a triangle. Note that a closed triangle, made up of \( u, v, w \in \hat{V} \), can be represented as six different tuples: \((u, v, w), (u, w, v), (v, u, w), (v, w, u), (w, u, v), (w, v, u)\). All these tuples need to be counted. Note, that by using a lexicographical sorting, only three of these tuples need to be counted. This however, does not change the relevance of our approach as our algorithm benefits from the lexicographical sorting as well.

Lemma 1: At least two out of the three vertices that make up a triangle are within the vertex cover, \( \hat{V} \). By contradiction, consider the case that only one vertex is in
Algorithm 1: Pseudo-code for computing clustering coefficients given a vertex cover \( \hat{V} \).

```plaintext
triangles ← 0;
for \( v ∈ \hat{V} \) do
  for \( u ∈ \text{adj}(v) \) do
    if \( u = v \) then
      next u;
    else
      \( C ← \text{intersect}(v, \text{adj}(c), u, \text{adj}(v)) \);
      for \( c ∈ C \) do
        if \( c ∈ \hat{V} \) then
          triangles ← triangles + 1;
        else
          triangles ← triangles + 3;
```

As a results of these two Lemmas, we only need to intersect the adjacency lists of two adjacent vertices when both vertices are marked in the vertex cover. That is, for \( u, v ∈ V \land (u, v) ∈ E \) such that either:

1) \( v ∈ \hat{V} \land u \notin \hat{V} \)
2) \( v \notin \hat{V} \land u ∈ \hat{V} \)

the intersection between these vertices is not required.

In the original algorithm, this intersection is indeed computed. We now show that it is only necessary to intersect adjacency lists of adjacent vertices that are both in the vertex cover.

We denote \( C \) as the set of the intersections between the two vertices. If \( C \) is empty, these vertices do not have common neighbors. If \( C \) is not empty then for each \( w ∈ C \) the following two scenarios can happen: 1) \( w ∈ \hat{V} \) or 2) \( w ∉ \hat{V} \). Both these scenarios are depicted in Fig. 1.

Using the above observations we show that it is possible to compute the clustering coefficient by intersecting the adjacency lists of two vertices \( u, v \) such that \( u, v ∈ \hat{V} \land (u, v) ∈ E \). We are required to show that all six tuples are accounted for. We start with the case that \( w ∈ C \land w ∈ \hat{V} \), as such \( u, v, w ∈ V \). When intersecting \( u \) with \( v \), \( w \) is found. When intersecting \( v \) with \( u \), \( w \) is found again. The intersection will be repeated for intersecting \( u \) with \( v \), \( v \) with \( w \) and their respective reverse orders. For each of the triangles found, the global triangle counter is increased. All six tuples have been accounted for.

For the second scenario when \( w ∈ C \land w ∉ \hat{V} \), the only intersection computed is between vertices \( u \) and \( v \) and vertices \( u \) and \( v \) is not intersected with other vertices as it is not in the vertex cover. As such, some intersections are omitted. Yet, the omitted intersections can be accounted for by a simple counting correction where each intersection is counted as 3 different triangles. Given the two computed intersections, \( u \) with \( v \) and \( v \) with \( u \), all the six triangles are properly accounted for.

Given the above, it is possible to create an algorithm for computing the clustering coefficients given a vertex cover that reduces the number of neighbor intersections. The pseudo-code for this is given in Algorithm 1. Note the algorithm assumes that a vertex cover has been computed. We extend the discussion on the time complexity of vertex covers in a following subsection.

### B. Complexity Analysis

In this section we do a complexity analysis of our new algorithm and discuss the cost of computing the vertex cover in terms of time complexity. The time complexity of the original algorithm is \( O(V \cdot d_{\text{max}}^2) \) where \( d_{\text{max}} \) is the size of the largest adjacency list in the graph. The key difference between our new algorithm and the original algorithm is that only a subset of vertices requires adjacency list intersection: \( \hat{V} \). Further, only the adjacency lists of vertices that are within the vertex cover are intersected. As such the \( O(d_{\text{max}}^2) \) is replaced with \( O(d_{\text{max}}^2) \), where \( d_{\text{max}} \) is the size of the largest adjacency list in the vertex cover.

Intuitively, the computational bottleneck of the original algorithm is the vertex with the largest adjacency list. This is because this vertex is intersected with all its neighbors \( d_{\text{max}} \) times and each intersection requires \( d_{\text{max}} \) operations. Conceptually this is still the same for the new algorithm, however, the bottleneck is no longer the highest degree vertex in the graph, but, rather the highest degree vertex in the vertex cover. This gives the new algorithm a time complexity of \( O(Time_{\text{VC}} + \hat{V} \cdot d_{\text{max}}^2) \), where \( O(Time_{\text{VC}}) \) refers to the time complexity of finding a vertex cover.

For the new algorithm to be considered optimal and not to add additional overhead to the existing complexity, the following requirements needs to be met: \( O(Time_{\text{VC}}) \leq \)
\( O(V \cdot d_{max}^2) \). For practical purposes, an even tighter bound is needed: \( O(Tim_{vc}) \leq O(V \cdot d_{max}^2 - V \cdot d_{max}^2) \). These requirements imply that the time spent computing the vertex cover should not be greater than the reduced run-time.

While a trivial vertex cover of \( \tilde{V} \equiv V \) can be found in \( O(1) \), this is essentially the original algorithm. Linear time approximations yield reasonable smaller vertex covers than \( V \) in practice. For most linear-time vertex cover algorithms the condition is met almost trivially as \( O(V + E) \) is smaller than \( O(V \cdot d_{max}^2) \). This is not true for considerably sparse graphs, where \( E \approx 2 \cdot V \), as very little work is required for computing the list intersections. We extend this discussion in Section IV.

C. Circuits of length 4 and higher

A circuit is defined as a simple path of vertices where each vertex and edge is traversed once with the first and last vertices in the path being the same. For example, a triangle is a 3-circuit. Abdou et al. [3] suggest using larger circuits than 3-circuits for the purpose of graph analysis. Specifically, they discuss the impact of “long-distance” relationships on the underlying graph structure.

A circuit of length four can be considered a square and a circuit of length five can be considered a pentagon. From a social network analysis standpoint, finding a square in the network given a specific player, \( v \), is equivalent to finding common friends of any two of \( v \)’s friends. This can be extended to find circuits of all sizes.

Fig. 2 depicts a subset of different vertex covers for a square. In all these vertex covers, \( u \) and \( x \) are part of the cover. All non-relevant edges have been removed. Note, in all the examples of Fig. 2 at least one set of vertices that are across from each other (i.e., two-hops away) are in the vertex cover. This is mandatory for the vertex cover to be maintained. In our examples, these vertices are \( u \) and \( x \). We refer to these vertex-pair as cross-vertices.

Finding circuits of length four entails taking all vertex pairs and intersecting their adjacencies. Given the adjacency set of two cross-vertices \( C \), if \( |C| > 2 \) (meaning that the vertices share two or more neighbors) then a square exists. To be exact there are \(|C| \cdot (|C| - 1)\) different 4-circuits. We will elaborate on this.

The pseudo-code for finding the number of 4-circuits using the vertex cover approach can be found in Alg. 2. The pseudo-code makes the proper counting correction. Using the cross-vertices observation, we reduce the computed intersection from “all vertex pairs” to “all vertex-cover pairs” without missing a 4-circuit. To get the original algorithm for 4-circuits, the pseudo code requires a simple modification: replace \( \tilde{V} \) with \( V \) in the first two loops and get rid of the counting correction code.

Given \( u, x \) are cross-vertices (not necessarily in the vertex cover), now consider the following scenarios when \( w, v \in V \) and \( u, w, v \) are cross-vertices (both pairs must be cross vertices for a square to exist). The following scenarios occur:

1) \( w, v \notin \tilde{V} \). If they are part of the 4-circuit, then they can be found when intersecting the adjacency list of \( u, x \) and therefore this intersection can be avoided as it is redundant. If they are not part of a 4-circuit, but simply vertices that are two hops away, then there is no point intersecting their adjacencies allowing a reduced number of intersections.

2) Either \( w \in \tilde{V} \land v \notin \tilde{V} \) or \( v \in \tilde{V} \land w \notin \tilde{V} \). Without the loss of generality, assume the second case. Once again, for \( w, v \) to be part of a 4-circuit, \( u, x \in \tilde{V} \) cross vertices need to be adjacent to them. For the same reasons as in 1), redundant intersections can be avoided.

3) \( w, v \in \tilde{V} \). It is possible that they are part of a 4-circuit. If \( u, x \in \tilde{V} \) are cross vertices, then they are
part of a 4-circuit. Therefore, their adjacencies need to be intersected. As such, the 4-circuit will be found for the intersection of \( w \) with \( v \), \( w \) with \( u \), \( u \) with \( x \), and \( x \) with \( u \).

If \( u, x \in \hat{V} \), it is possible to swap roles between \( w, v \) and \( u, x \) and go back to 1) which states that the only intersections that need to be computed are between the vertices in the cover.

The above essentially states the counting corrections that need to be made.

Assume that \( u, x \in \hat{V} \), meaning that they are potentially cross-vertices and need to have their lists intersected. In the process of intersecting their adjacencies lists, the set \( C \) consists of two subsets, the first set consists of vertices in the vertex cover and is denoted \( in\hat{V} \), and the second set consists of vertices that are not in the vertex cover, denoted as \( not\hat{in}\hat{V} \). Fig. 3 depicts the intersections of two vertices \( u \) and \( x \). The vertices belonging to \( in\hat{V} \) have been marked with a double circle.

The number of ordered pairs in the intersection is \(|C| \cdot (|C| - 1)\), each one of them specifies a different 4-circuit. Remember, that \( u \) will be intersected with \( x \) and \( x \) will be intersected with \( u \) and these give different 4-circuits 1.

Because not all vertex pairs have their lists intersected, a counting correction needs to be taken into account. Consider \( v, w \) common neighbors of the ordered pair \((u, x)\). If \( v, w \in \hat{V} \), then the ordered pair \((v, w)\) will have its neighbors intersected and then \( u, x \) will be found. Therefore, no correction needs to be done.

If \( v, w \not\in \hat{V} \), then the ordered pair \((v, w)\) will not have its adjacencies intersected, therefore, requiring a counting correction as if the adjacencies of the ordered pair \((v, w)\) were intersected and found \( u, x \) as its common neighbors. This adds two additional 4-circuits because of the ordering of the neighbors. The ordered pair \((w, v)\) also will not have its neighbors intersected; however, this counting correction is taken into account by the ordered pair \((x, u)\). Therefore, for the 4-circuit consisting of vertices \( u, v, w, x \) all eight 4-circles are accounted for. The counting correction needed because of vertices that are not in the vertex cover is \(|not\hat{in}\hat{V}| \cdot (|not\hat{in}\hat{V}| - 1)\).

For the case in which only one of \( w, v \) is in the vertex cover, the explanation from above repeats itself. Therefore, the number of cycles that need to be accounted for is essentially the number of ways to order \( w, v \): \( 2 \cdot |in\hat{V}| \cdot (|not\hat{in}\hat{V}| - 1)\).

In this subsection we showed how to find 4-circuits using the vertex cover and applying additional counting correcting techniques. These techniques can be further extended to include the general \( K \)-circuit case for \( K \geq 3 \). However, the general \( K \)-circuit case is outside the scope of this paper.

D. Clustering Coefficients in Dynamic Graphs

In this subsection we show that our new vertex cover clustering coefficients approach can also be applied to dynamic graphs. The two types of operations that we consider for dynamic graphs are edge insertions and edge deletions. Vertex insertion and deletion are simple. A vertex insertion places a new vertex in the graph without any edges. A vertex deletion can be serialized as a set of edge deletions.

For an inserted edge, \( e = (u, v) \), into the graph the following three scenarios can arise:

1) \( u, v \not\in \hat{V} \)
2) \((u \in \hat{V} \land v \not\in \hat{V})\) or \((v \in \hat{V} \land u \not\in \hat{V})\)
3) \( u, v \in \hat{V} \)

The global and local clustering coefficients are handled slightly differently. Again, we focus on the global case.

For the first case, where both vertices are not in the vertex cover, one of the vertices has to be added to the vertex cover to ensure that all edges are covered. For simplicity assume that \( v \) is added to the vertex cover. The inserted edge can potentially close several triangles. The second case is similar to the first but does not require modifying the vertex cover. Obviously, for the third case when both vertices in the vertex cover, the vertex cover does not require any modification.

Edge deletions can be dealt with in a similar fashion. However, when deleting \( e = (u, v) \), the first scenario in which \( u, v \not\in \hat{V} \) is not possible as this violates the vertex cover’s properties.

IV. Empirical Results

In this section we present empirical performance results of the new clustering coefficients algorithm for both triangle counting and square counting. In our tests we used an Intel i7-2600K system with 16GB of memory. The clock frequency is 3.4GHz. We used graphs taken from the 10th DIMACS Implementation Challenge on Graph Partitioning and Graph Clustering [4]. The graphs we used can be found in Table II. The global clustering coefficient value for these graphs are presented in Fig. 4.

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For benchmarking purposes, we used the clustering coefficient algorithm from [13]. This is a highly optimized CSR implementation of clustering coefficients. As such we have used this CSR clustering coefficient implementation as our baseline and have implemented our new algorithms based using the list intersection taken from the implementation.

We use is a simple linear time greedy algorithm, $O(V+E)$ for finding the vertex cover. For each vertex $v$ in the graph, we check if $v$ has any neighbors that are not within in the vertex cover. If this is the case, $v$ is added to the vertex cover. This algorithm by no means ensures a small or minimal vertex cover. Nonetheless, for our needs it is sufficient as it gave small enough vertex covers for us to see improvements.

A. Static Clustering Coefficients

The overhead to compute a reasonable vertex cover is negligible for many graphs. Fig. 5 depicts the portion of time needed to compute the vertex cover out of the total needed for computing the clustering coefficients. Note that the ordinate of Fig. 5 is a log-scale which shows the time spent computing the vertex cover out of the total time of the new algorithm (time for the vertex cover and the clustering coefficient algorithm that uses the vertex cover), and lower is better. In Fig. 5 an additional (constant) curve has been added at 2.5%. For many of the graphs, the vertex cover takes less than 2.5% of the total time to compute.

Fig. 6 depicts the size of the vertex cover in comparison with the entire vertex set $\hat{V}$. An additional constant curve has
The size of the 70% to 10% cover takes such a high percentage of the total time is due to six graphs, four are road networks. The reason the vertex (by a different algorithm) might possibly reduce the size of our new approach. These are graphs for which the vertex does not benefit from run-time. An additional blue curve has been added to the figure to state when the new algorithm is faster, as such lower is better.

Figure 7. The ordinate is the time ratio of the new vertex cover-based clustering coefficients algorithm and the original clustering coefficient algorithm. The time of the new algorithm includes both the time needed to find the vertex cover and the time for computing the clustering coefficients. All bars below the blue curve occur when the new algorithm is faster, as such lower is better.

Figure 8. The ordinate is the ratio of the number of intersection that are necessary by the new algorithm in comparison with the original algorithm.

The actual number of elements intersected for both methods. The blue solid bars are the ratio between using the new method and the respective number using the original one. The green patterned bars are values from approach as our vertex cover added overhead. Fortunately, this overhead is negligible.

In Fig. 8 two different bar charts are shown, both are ratio values from 0 to 1, such that the ratios are between our new algorithm and the original one. The green patterned bars are the ratio between the number of adjacency lists intersected using the new method and the respective number using the original method. The blue solid bars are the ratio between the actual number of elements intersected for both methods.

Figure 9. The ordinate presents the ratio of time spent finding the vertex cover as a function of the total time spent finding all the 4-circuits. Note that the ordinate is a log scale. The abscissa are the Clustering graphs from Table II.

been added at 70%. For slightly more than half the graphs, the size of the vertex cover is less than 70% the size of the entire vertex set.

Fig. 7 depicts the ratio of the time it takes to compute the global clustering coefficients using our new method over the original algorithm. For this figure, lower is better as this means that the new algorithm takes a fraction of the original run-time. An additional blue curve has been added to the figure to state when the performance of the two algorithms is the same. There are several graphs that do not benefit from our new approach. These are graphs for which the vertex cover is roughly the size of $|V|$. A different vertex cover (by a different algorithm) might possibly reduce the size of the vertex cover and allow for shorter run times. However, this was not the focus of our work.

For six graphs that we tested, the time for finding the vertex cover took over 10% of the total time. Out of these six graphs, four are road networks. The reason the vertex cover takes such a high percentage of the total time is due to the considerable sparsity of these graphs, where $|E| \leq 2 \cdot |V|$. These graphs have low clustering coefficients and the amount of intersections needed is considerably small. It is not surprising that for these graphs the total time of our new algorithm is greater than the time using the original approach as our vertex cover added overhead. Fortunately, this overhead is negligible.
These two ratios are slightly different. The first ratio states how many intersections need to be computed. The second ratio states how many comparisons need to be done as part of the list intersection. The second ratio takes into account that the lists are not equal length. This is expected in graphs following power-law distribution [6], [9], [16]. For graphs with a uniform distribution of edges, such as the Erdős-Rényi ([14], [15]) random graph model, it is very likely that these ratios would be similar. Further this non-uniformity plays a critical part of the time complexity of the algorithm, specifically $d_{max}$.

As expected, the graphs that had larger vertex covers (when $|V|\gg|V|$) did not have a reduction in the number of intersections or the number of elements intersected. The graphs with the smaller vertex covers had fewer intersections, fewer comparisons, and reduced runtime.

As the number of intersections of our algorithm is bounded by the number of intersections of the original algorithm (which computes all the intersections) these ratios are smaller than one.

In the context of performance, the smaller these ratios are the better the performance will be as these ratios are correlated to the number of list intersections that need to be done. As such, the lower the ratios, the better.

### B. Static 4-Circuits

In this subsection we present performance results for finding 4-circuits using the algorithm presented in Section III-C. A key challenge of the 4-circuit counting is the increased time complexity of the algorithm. As such, for the 4-circuits counting we present results for the smaller graphs that belong to the clustering subset of graphs taken from Table II. We note that finding the 4-circuits for larger graphs can be parallelized to further reduce the running time.

Fig. 9 depicts the time spent computing the vertex cover vs. the time to compute the vertex cover and find the 4-circuits. For almost all the graphs, the time to compute the vertex cover is less that 0.1%. For several graphs the vertex covers takes less than 0.001% of the total time. We use the same vertex cover algorithm as before and the running times are the same as before. As the circuits get longer the time spent on finding the vertex cover becomes a smaller fraction of the total running time.
Fig. 10 depicts the time ratio between the new algorithm (including time spent on finding the vertex cover) and the original algorithm for computing 4-circuits. The blue curve at $y = 1$ denotes when the execution time of both algorithms is the same. Once again, any bar below the blue curve means that the new algorithm is faster than the original algorithm. For half the graphs the new algorithm is 30\% faster than the previous algorithm which is due to fewer intersections. Fig. 11 depicts the ratio of the number of comparisons made during the list intersections for the new and original algorithms.

V. Conclusions

In summary, in this paper we design and implement a new method for computing exact clustering coefficients using vertex covers. This method reduces the number of list intersections and avoids unnecessary element comparisons. The two key differences between our new algorithm and the original approach are as follows: 1) our algorithm computes a vertex cover of the graph and 2) our algorithm applies a counting correcting technique that makes up for triangles that are not counted multiple times.

We show that the new algorithm is both exact (gives the same results as the original algorithm) and correct (by proofs). We then show that our new approach can also be used on circuits of length four and can be extended to longer circuits. All these are followed by performance analysis in which we showed that the new algorithm is indeed faster, 15\% -- 20\% for the 3-circuit and 30\% -- 40\% for the 4-circuit, than the previous algorithms.

While the focus of work was to show the viability of the vertex cover for clustering coefficients, we believe that the vertex cover approach might be applicable to additional social network analytics, perhaps community detection and modularity-based algorithms. Several open problems related to this work are:

1) Finding additional analytics that can benefit from the vertex cover.
2) Testing the sensitivity of the algorithm by changing the vertex cover algorithm. This includes using additional vertex cover algorithms or using the same vertex cover algorithm as we did that accesses the vertices in a different order (this should give a different cover).
3) Similar to the previous issue, is it possible to select a vertex cover algorithm based on properties of the input graph.
4) For the dynamic graph case, how does the vertex cover change over time? Does the vertex cover gradually increase in size over time until its the size of the vertex set or is the vertex cover near constant over the insertion?
5) Creating an efficient algorithm for finding a $K$-circuit for some given $K$ using the vertex cover approach.
6) Is there an effective way to parallelize the new algorithm?

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