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March 26, 2014

College of Computing Picks Bader to Lead School of CSE

March 26 — Following a national search for new leadership of its School of Computational Science and Engineering (CSE), (http://www.cse.gatech.edu/) Georgia Tech's College of Computing has selected its own David A. Bader, a renowned leader in high-performance computing, to chair the school.

Bader (http://www.cc.gatech.edu/~bader/), a professor in the School of CSE and executive director of the High-Performance Computing Lab, succeeds Regents' Professor Richard Fujimoto (http://www.cse.gatech.edu/people/richard-fujimoto), who has served in the role since 2007 and through CSE's elevation to "school" status in 2010. Fujimoto has continued to serve as chair amid the search and will remain as a professor within the school.

Bader will assume his new role in July 2014. His appointment is contingent upon approval by Georgia Tech President G.P. "Bud" Peterson and the Board of Regents of the University System of Georgia.

"I'm thrilled that we found within our own ranks a candidate as viable as David to take the helm of the School of CSE," said Zvi Galil, John P. Imlay Jr. Dean of Computing. "Not only does David bring a deep background of experiences and an ambitious agenda that will ensure our college remains at the forefront of high-performance computing and data analytics, he exudes a contagious level of energy for these areas of computing. We expect great things to continue in the school under David's watch."

Bader earned his Ph.D. in electrical engineering from the University of Maryland, College Park, in 1996 after earning both a B.S. in computer engineering in 1990 and an M.S. in electrical engineering in 1991 from Lehigh University. During his doctoral research, he was a NASA graduate fellow and was awarded a National Science Foundation research role in experimental computer science after receiving his doctorate.

In 1998, he became an assistant professor and Regents' lecturer at the University of New Mexico. In 2005 he joined Georgia Tech's College of Computing, where he became a full professor in 2008 and an integral part of the formation of the School of CSE.

"I believe that the School of CSE has firmly established itself as the country's premier department focused on solving real-world challenges through advanced computational techniques, thanks to a world-class faculty and dedicated students," Bader said. "My plan as school chair is to accelerate our impactful research and gain recognition for further successes by solving grand challenges that make this world a better place for all."

While at Georgia Tech, Bader has established himself as an expert in the design and analysis of parallel and multicore algorithms for real-world applications, including computational biology and genomics and massive-scale data analytics.

For his work (http://www.cc.gatech.edu/~bader/resume.html), he has earned recognition as a fellow with both IEEE and AAAS, received a National Science Foundation CAREER award and numerous industry aw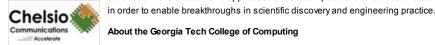ards from IBM, NVIDIA, Intel, Cray, Oracle/Sun Microsystems and Microsoft Research. InsideHPC recognized him as a "RockStar" of high-performance computing, and HPCwire's cited him among "People to Watch" (http://www.cse.gatech.edu/content/david-bader-selected-one-hpcwire%E2%80%99s-%E2%80%9Cpeople-watch%E2%80%9D-2014) in both 2012 and 2014.

Bader serves as a board member of the Computing Research Association (CRA), on the NSF Advisory Committee on Cyberinfrastructure, on the Council on Competitiveness High Performance Computing Advisory Committee, on the IEEE Computer Society Board of Governors and on the steering committees of the IPDPS and HiPC conferences. He is editor-in-chief of IEEE Transactions on Parallel and Distributed Systems (TPDS) and program chair for IPDPS 2014.

The School of Computational Science and Engineering is devoted to the advancement and promotion of the CSE discipline. Research focuses on making fundamental advances in the creation and application of new computational methods and techniques in order to enable breakthroughs in scientific discovery and engineering practice.

About the Georgia Tech College of Computing

Along These Lines

Making HPC and Big Data More Accessible
(http://www.hpcwire.com/2014/03/25/making-hpc-big-data-accessible/)

Tianhe-2, The Arena for ASC14 Student Supercomputer Challenge
(http://www.hpcwire.com/2014/03/27/tianhe-2-arena-asc14-student-supercomputer-challenge/)

Swiss Hybrid Petaflopper Opens for Research
(http://www.hpcwire.com/2014/03/24/swiss-hybrid-petaflopper-opens-research/)

Peek into China's Plans for Top Supercomputer Shows No Slowdown
(http://www.hpcwire.com/2014/03/20/peek-chinas-plans-top-supercomputer-shows-slowdown/)

Feature Articles

This Week in HPC News (http://www.hpcwire.com/2014/03/28/8645/)
This week's HPC news wrap comes from high above the U.S. during a fly-back from the GPU Technology Conference, which took place

The Georgia Tech College of Computing is a national leader in the creation of real-world computing breakthroughs that drive social and scientific progress. With its graduate program ranked ninth nationally by U.S. News and World Report, the College's unconventional approach to education is defining the new face of computing by expanding the horizons of traditional computer science students through interdisciplinary collaboration and a focus on human-centered solutions. For more information about the Georgia Tech College of Computing, its academic divisions and research centers, please visit http://www.cc.gatech.edu (http://www.cc.gatech.edu/).

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Source: Georgia Tech College of Computing

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in San Jose, Read more… (http://www.hpcwire.com/2014/0...)

NVIDIA Highlights GPU Progress on Titan Supercomputer (http://www.hpcwire.com/2014...highlights-gpu-progress-titan-supercomputer/)
The GPU Technology Conference this week in San Jose offered plenty of material for the supercomputing set with a number of presentations focused Read more… (http://www.hpcwire.com/2014/0...highlights-gpu-progress-titan-supercomputer/)

Tianhe-2, The Arena for ASC14 Student Supercomputer Challenge (http://www.hpcwire.com/2014...2-arena-asc14-student-supercomputer-challenge/)
A few days ago, 2014 ASC Student Supercomputer Challenge (hereafter ASC14) announced that the 16 ASC14 finalists will compete an

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Making HPC and Big Data More Accessible (http://www.hpcwire.com/2014/03/25/making-hpc-big-data-accessible/)
Dating back to 2009, the SGI Altix UV line has always targeted performance and scalability. In this recent video, SGI addresses how this Read more… (http://www.hpcwire.com/2014/03/25/making-hpc-big-data-accessible/)

The Big Data of Holistic Performance Analysis (http://www.hpcwire.com/2014/03/19/big-data-holistic-performance-analysis/)
Without the possibility of revolutionary new shifts in large-scale computing on the hardware horizon, an increasing amount of attention is being Read more… (http://www.hpcwire.com/2014/03/19/big-data-holistic-performance-analysis/)

Is the ECC Performance Price Worth it for GPUs? (http://www.hpcwire.com/2014/03/13/ecc-performance-price-worth-gpus/)
Listen to the podcast followup on this story. (http://www.hpcwire.com/soundbite...performance-price-gpus/)

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Spotlight ON: HPC in Commercial Applications (http://tci.taborcommunications.com/l/21812/2014-03-14/56hkm)

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PBS Professional Manages Workload for NCI Raijin, Largest Supercomputer in Southern Hemisphere (http://www.hpcwire.com/whitepaper/pbs-professional-manages-workload-nci-raijin-largest-supercomputer-southern-hemisphere/)

2/24/14 | Altair | Australia's National Computational Infrastructure (NCI) chose PBS Professional to manage workload for its 'Raijin' cluster from Fujitsu, the Read more… (http://www.hpcwire.com/whitepaper/pbs-professional-manages-workload-nci-raijin-largest-supercomputer-southern-hemisphere/)

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